Creating the future of Automobiles

Automotive Regulator Selection Guide

Rev. 1.2

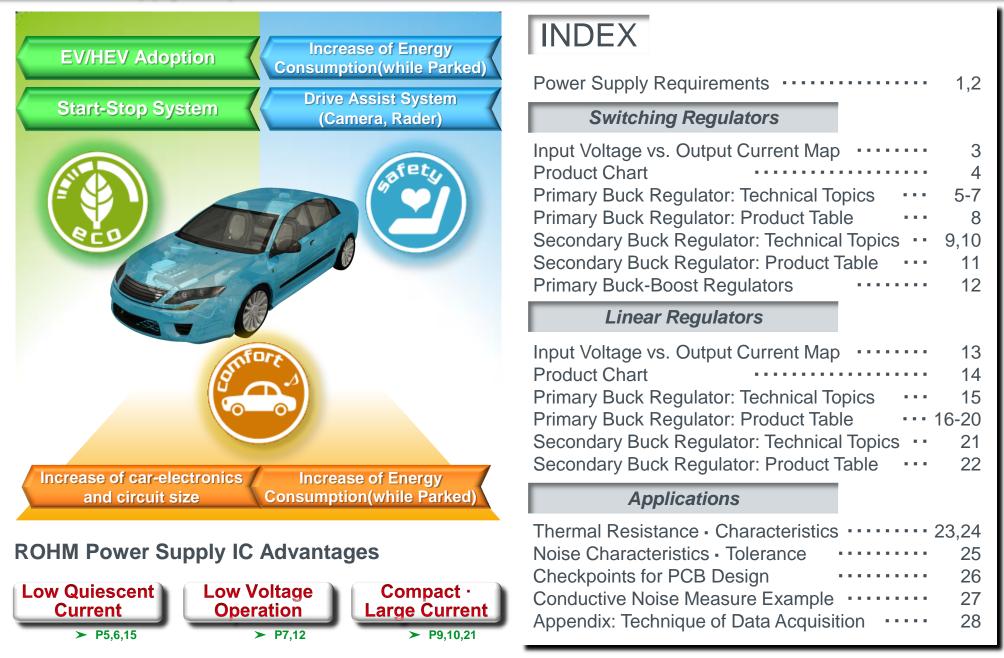


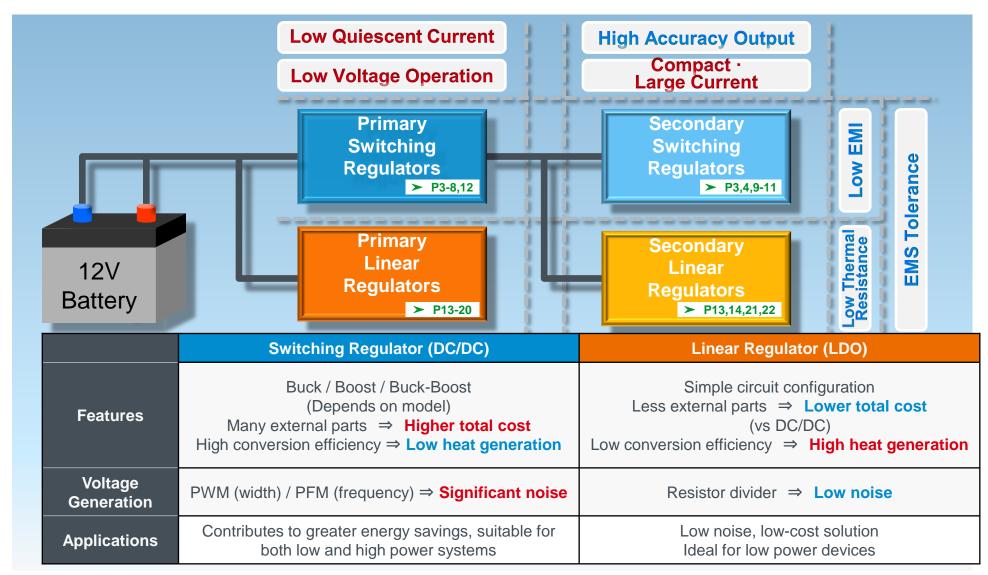






Power Supply Requirements the Drive Automotive Evolution





ROHM Power Supply IC Advantages

Effective Anti-Noise Technology

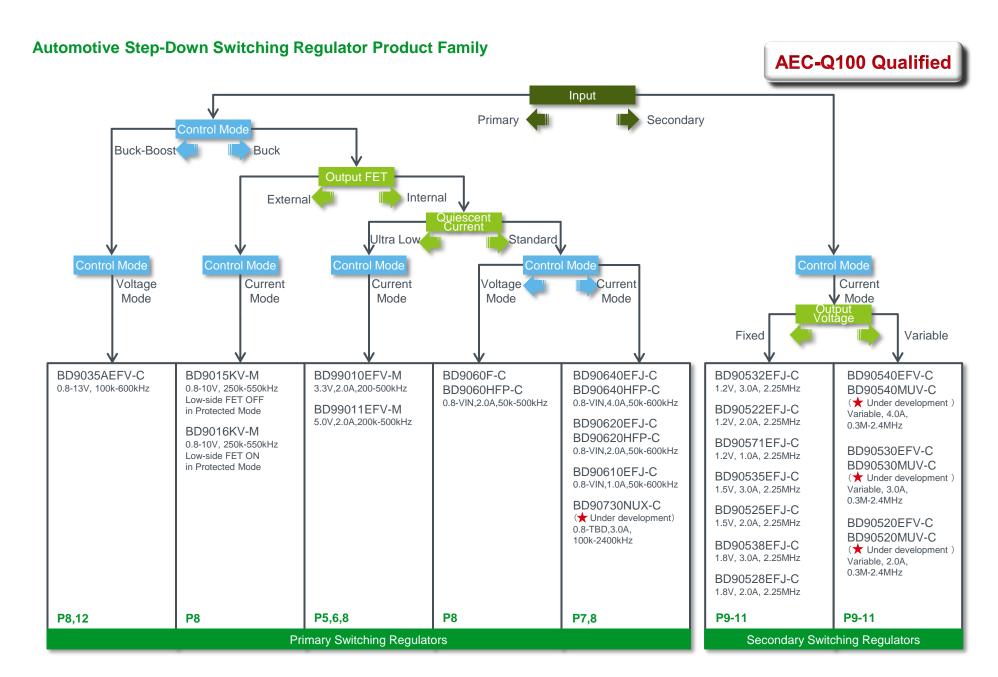
Superior Heat Dissipation Technology

Noise Characteristics - Tolerance ➤ P25-27

Thermal Resistance - Characteristics ➤ P23,24

Automotive Step-Down Switching Regulator Lineup





Primary Switching Regulators (BD9901xEFV-M Series)

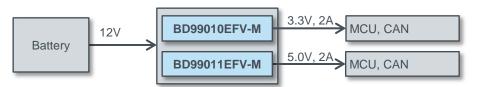
Low Quiescent Current

Low Quiescent Current Solutions

The BD99010EFV-M and BD99011EFV-M are low Iq step-down DC/DC converters that integrate a power MOSFET and provide 3.3V and 5V output, respectively. SLLMTM (Simple Light Load Mode) is included, ensuring low current consumption and

SLLMTM (Simple Light Load Mode) is included, ensuring low current consumption and high efficiency at light loads as well as high efficiency during heavy loads while providing regulated output voltage. In addition, the ICs are compliant with automotive standards and support a maximum voltage of 42V.

A minimum input voltage of 3.6V maintains output when cold cranking, and current mode control delivers fast transient response and easy phase compensation. Both models are available in a HTSSOP-B24 package, making them ideal for applications requiring few external components and a small PCB footprint.



Product Overview: BD9901xEFV-M

Low Quiescent Current

• Low quiescent current: 22µA (Typ.)

High Efficiency

Synchronous rectification (No external Schottky diode required)

Simple Light Load Mode (SLLMTM)

Supports Cold Cranking Operation (3.6V Operation)

Input voltage range: 3.6V to 35V (42V rating)

(Initial startup over 3.9V)

HTSSOP-B24

● Output voltage: 3.3V±2% (BD99010EFV-M)

5.0V±2% (BD99011EFV-M)

Switching output current: 2A (Max.)

• Switching frequency: 200k to 500kHz

• Integrated switching FET: Pch $170m\Omega$ (Typ.), Nch $130m\Omega$ (Typ.)

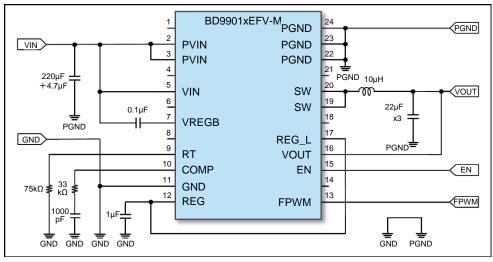
Soft Start built in

• Enable pin compatible with CMOS logic and battery voltages

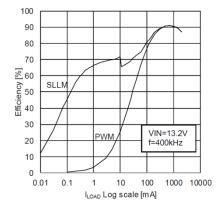
Forced PMW Mode function

Current Mode control with external compensation circuit

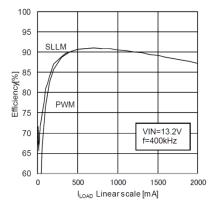
 Over Current/Short Circuit protection, VOUT Over Voltage protection, Under Voltage Lock Out, and thermal protection circuits



BD9901xEFV Application Circuit



BD99011EFV-M Efficiency vs Load Current VIN=13.2V , VOUT=5.0V (Log scale)

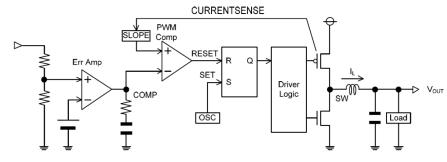


BD99011EFV-M Efficiency vs Load Current VIN=13.2V , VOUT=5.0V (Linear scale)

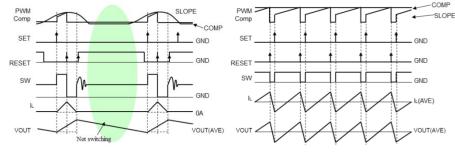
SLLM™ (Simple Light Load Mode) Control

During lighter loads operation automatically switches to Simple Light Load Mode (SLLMTM), which utilizes PWM control and compares the output voltage to an internal reference voltage. When the output voltage drops below the reference voltage switching pulses are output to increase the voltage above the reference level, after which the SW output turns off and the controller goes into a very low current consumption standby mode until the output voltage dips below the reference voltage again.

When the time between switching pulse skips becomes short the devices exit SLLMTM mode and resume normal continuous switching operation. The load level of the switching pulse skip can be adjusted by the input voltage and inductor value.

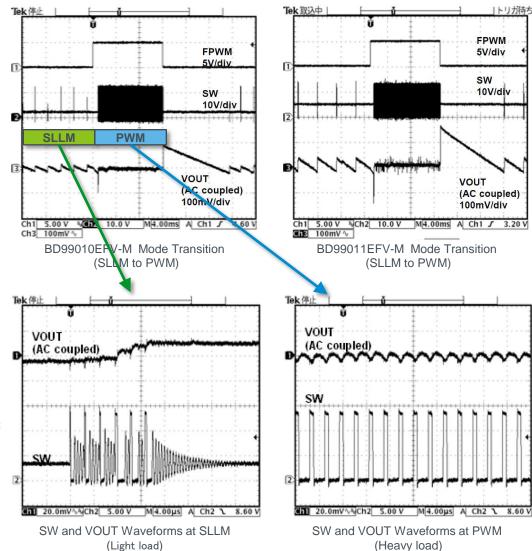


Current Mode PWM Control Diagram



SLLM Switching Timing Chart

PWM Switching Timing Chart



SLLMTM control at light loads differs from regular PWM, resulting in higher output ripple voltage. Also, during SLLMTM the transient response for heavy loads is slower.

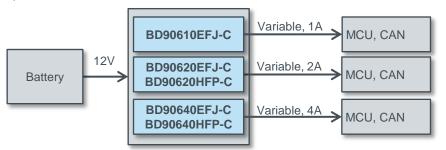
Primary Switching Regulators (BD906xxEFJ-C Series)

Low Voltage Operation

Low Voltage Operation Solutions

The BD906xxEFJ-C series of step-down switching regulators integrate a high voltage power MOSFET and make it possible to easily set the operating frequency via external resistor. Features include wide input voltage (3.5V to 36V) and operating temperature (-40°C to +125°C) ranges, along with an external synchronization input pin that enables synchronous operation via external clock.

In addition, the internal Pch MOSFET can operate at 100% ON duty to ensure stable operation even during severe battery drops during conventional cranking or idling stop operation.



BD906xxEFJ-C PVIN SW VIN 220μF 4.7μF VEN/SYNC GND SW 15μH 15μ

BD906xxEFJ-C Application Circuit

Product Overview: BD906xxEFJ-C/HFP-C

Wide Input Voltage Range

Circuit current at shutdown:

• Input voltage range: 3.5V to 36V (42V rating)

(Initial startup over 3.9V)

• Built-in Pch FET enables 100% duty

luty *HTSOPJ-8* 0µA (Тур.) *BD906xxEFJ-C*

• Reference voltage: 0.8V±2% (Ta: -40°C to +125°C)

0.8V±1% (Ta: 25°C)

Switch output current:
 1.25A Max. (BD90610EFJ-C)

2.5A Max. (BD90620EFJ-C/HFP-C)

4A Max. (BD90640EFJ-C/HFP-C)

• Switching frequency: 50k to 600kHz

Internal switching FET: Pch 160mΩ (Typ.)

Soft Start function

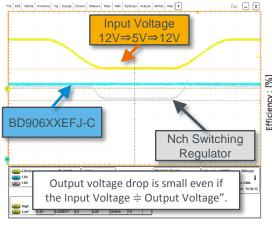
• Enable pin compatible with CMOS logic and battery voltages

• Current mode control with external compensation circuit

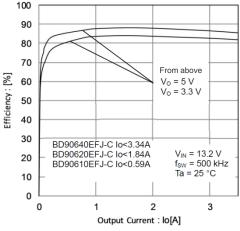
 Over Current/Short Circuit protection, Under Voltage Lock Out, and Thermal Shutdown circuits



HRP7 BD906xxHFP-C



BD906xxEFJ-C Output Waveform in Input Voltage Change



BD906xxEFJ-C Efficiency vs Load Current VIN=13.2V

Primary Switching Regulator Selection Guide

| | | | | | | | | | | | | | | | | | Func | tions | | | | | |
|--------------|--------------------------|----------------------|--------------------|-------------------------|--------------------------|------|----------------------|-------------------------------|--|------------------|-----------------|-----------------|---|---------|------------------|----------------------|----------------------|------------------|---------------------|-------------------|---------------------|-----------------------------------|------------|
| Part No. | Number of Channels | Outpu | ıt FET | Rated Voltage (V) | Output Current (A) | Rai | /oltage nge /) | Output Voltage (V) Typ. | Reference (Output) Voltage Accuracy | Switch Freque | ~ | Control Mode | Operating Circuit Current (mA) | Good | l inization |) Irt | inous ation | Light ode | urrent on | I vn | tage on | Operating Temperature Range | Package |
| | G.1.4.11.10.10 | High Side (Typ.) | Low Side (Typ.) | (*) | Max. | Min. | Max. | . 75- | (%) | Range (kHz) | Accuracy (%) | | Тур. | Power (| External Synchro | Variable Soft Sta | Synchro Rectifica | Simple Load M | Over Cu Protecti | Therma Shutdov | Overvol Protecti | (°C) | |
| BD9015KV-M | 2 | Ext. Nch | Ext. Nch | 35 | - | 3.9 | 30 | Variable (0.8-10) | ±1.5 | 250 ~ 550 | ±10 | Current | 4 | 1 | 1 | ✓ | 1 | - | SR | SR | 1 | -40 ~ 105 | VQFP48C |
| BD9016KV-M | 2 | Ext. Nch | Ext. Nch | 35 | - | 3.9 | 30 | Variable (0.8-10) | ±1.5 | 250 ~ 550 | ±10 | Current | 4 | 1 | 1 | ✓ | 1 | - | SR | SR | 1 | -40 ~ 105 | VQFP48C |
| BD99010EFV-M | 1 | Pch (170mΩ) | Nch (130mΩ) | 42 | 2.0 | 3.6 | 35 | 3.3 | (±2.0) | 200 ~ 500 | ±20 | Current | 0.02 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 105 | HTSSOP-B24 |
| BD99011EFV-M | 1 | Pch (170mΩ) | Nch (130mΩ) | 42 | 2.0 | 3.6 | 35 | 5.0 | (±2.0) | 200 ~ 500 | ±20 | Current | 0.02 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 105 | HTSSOP-B24 |
| BD9060F-C | 1 | Pch (300m Ω) | - | 42 | 2.0 | 5.0 | 35 | Variable (0.8-VIN) | ±2.0 | 50 ~ 550 | ±5 | Voltage | 4.5 | - | 1 | - | - | - | SR | SR | - | -40 ~ 125 | SOP8 |
| BD9060HFP-C | 1 | Pch (300mΩ) | - | 42 | 2.0 | 5.0 | 35 | Variable (0.8-VIN) | ±2.0 | 50 ~ 550 | ±5 | Voltage | 4.5 | - | 1 | - | - | - | SR | SR | - | -40 ~ 125 | HRP7 |
| BD90640HFP-C | 1 | Pch (160m Ω) | - | 42 | 4.0 | 3.5 | 36 | Variable (0.8-VIN) | ±2.0 | 50 ~ 600 | ±10 | Current | 2.2 | - | 1 | ✓ | - | - | SR | SR | 1 | -40 ~ 125 | HRP7 |
| BD90640EFJ-C | 1 | Pch (160mΩ) | - | 42 | 4.0 | 3.5 | 36 | Variable (0.8-VIN) | ±2.0 | 50 ~ 600 | ±10 | Current | 2.2 | - | 1 | 1 | - | - | SR | SR | - | -40 ~ 125 | HTSOP-J8 |
| BD90620HFP-C | 1 | Pch (160m Ω) | - | 42 | 2.5 | 3.5 | 36 | Variable (0.8-VIN) | ±2.0 | 50 ~ 600 | ±10 | Current | 2.2 | - | 1 | 1 | - | - | SR | SR | - | -40 ~ 125 | HRP7 |
| BD90620EFJ-C | 1 | Pch (160mΩ) | - | 42 | 2.5 | 3.5 | 36 | Variable (0.8-VIN) | ±2.0 | 50 ~ 600 | ±10 | Current | 2.2 | - | 1 | 1 | - | - | SR | SR | - | -40 ~ 125 | HTSOP-J8 |
| BD90610EFJ-C | 1 | Pch (160mΩ) | - | 42 | 1.25 | 3.5 | 36 | Variable (0.8-VIN) | ±2.0 | 50 ~ 600 | ±10 | Current | 2.2 | - | 1 | 1 | - | - | SR | SR | - | -40 ~ 125 | HTSOP-J8 |
| BD90730NUX-C | 1 | Nch (180m Ω) | - | 42 | 3.0 | 3.5 | 36 | Variable (0.8-TBD) | ±2.0 | 100 ~ 2400 | ±10 | Current | 1.9 | 1 | 1 | 1 | - | - | L | SR | 1 | -40 ~ 125 | VSON10 |

★Under development ※ SR:Self Recovery , L:Latch HRP7 VQFP48C HTSSOP-B24 SOP8 VSON10 **ROHM** 7.8±0.1 (MAX 8.15 include BURR) 9.395±0.125 (MAX 9.745 include BURR) 5.0±0.2 (MAX 5.35 include BURR) 9.0±0.2 7.0±0.1 TABABABABABAÂ 0.595 0.17 +0.1 0.145 +0.05 1PIN MARK □ 0.1 S 0.22^{+0.05}_{-0.04} ⊕ 0.08 ₩ ...0.42±0.1 (Unit: mm) (Unit: mm)

Secondary Switching Regulators (BD905xx-C Series)

Compact Large Current

1.0µH / 1.5µH

%BD90571EFJ-C

Space-Saving High Efficiency Solutions

The BD905xx-C series of 2.25MHz fixed frequency synchronous rectification step-down DC/DC converters integrate a phase compensation circuit and feedback resistor that supplies a fixed output voltage of 1.2V/1.5V/1.8V, making it possible to configure applications with fewer external components.

The BD905x0 series consists of variable (0.3 to 2.4MHz) synchronous rectification step-down DC/DC converters featuring an internal external synchronization function and low current consumption (65uA) that enables high efficiency operation even at light loads.



1µF 22µF 2 VIN PGND 7 **%BD90571EFJ-C** 3 EN FB₆ $/47\mu\text{F} \times 1$ MODE 5 MODE 4 GND

1 PVIN

BD90571EFJ-C PCB

BD905xxEFJ-C Application Circuit

BD905x0EFV-C/MUV-C

2 PGND

3 SW

4 SW

5 N.C

PGOOD 20

CTL 19

SYNC 18

SEL 17

RT 16

N.C 15

FB 14

SS 13

COMP 12

GND 1

BD905xxEFJ-C

SW 8

Product Overview: BD905xxFEJ-C / BD905x0EFV-C/MUV-C

- Integrated output feedback resistors and phase compensation
- Synchronous rectification type
- Low current consumption: 65µA (Variable type)
- Selectable Light Load/PWM Fixed modes
- Switching frequency: 2.25MHz±20% (Fixed type)

0.3M to 2.4MHz±15% (Variable type)

External synchronous function(Variable type)

• Input voltage range: 2.6V (Variable)/2.69V (Fixed) to 5.5V (7V Rating) • Integrated SW FET: Pch 85mΩ (Typ.), Nch 70mΩ (Typ.) (Fixed type)

Pch $90m\Omega$ (Typ.), Nch $60m\Omega$ (Typ.) (Variable type)

Current mode control

• Over Current/Short Circuit protection, VOUT Over Voltage protection, Under Voltage Lock Out, and thermal protection circuits

| Output | | Output (Refere | nce) Voltage | |
|-------------------|-------------------------|----------------|--------------|--------------|
| Output Current | Variable (0.8V±1.5%) | 1.2V±2% | 1.5V±2% | 1.8V±2% |
| 1A | _ | BD90571EFJ-C | _ | - |
| 2A | ★BD90520EFV-C | BD90522EFJ-C | BD90525EFJ-C | BD90528EFJ-C |
| 3A | ★BD90530EFV-C | BD90532EFJ-C | BD90535EFJ-C | BD90538EFJ-C |
| 4A | ★BD90540EFV-C | _ | _ | - 1 |

★ Under development

BD905xxFEJ-C / BD905x0EFV-C/MUV-C Lineup



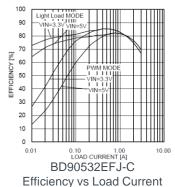
HTSSOP-B20

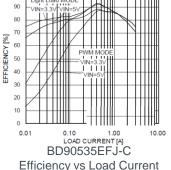


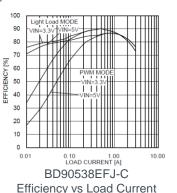
VQFN20SV4040

6 PVIN 7 PVIN 픞 8 VIN 9 EN 10 MODE

BD905x0EFV-C Application Circuit

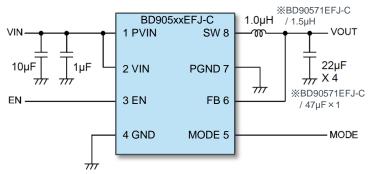




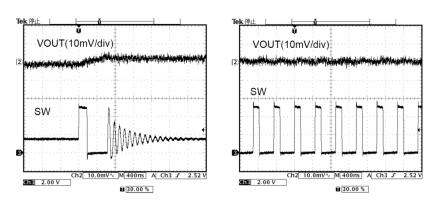


Light Load and PWM Fixed Modes

Setting the MODE pin voltage below 0.7V switches operation to Light Load Mode. Intermittent operation is automatically performed in Light Load Mode when the output load current is small. This suppresses switching loss, improving efficiency compared with PWM fixed mode. It should be noted that the load current during intermittent operation will change depending on the input voltage and inductor value. If the MODE pin is set to 2.1V or more PWM fixed mode operation is performed. And although efficiency is decreased at light loads compared with Light Load Mode, fixed frequency switching operation is implemented throughout the entire load range, making noise countermeasures relatively easier.

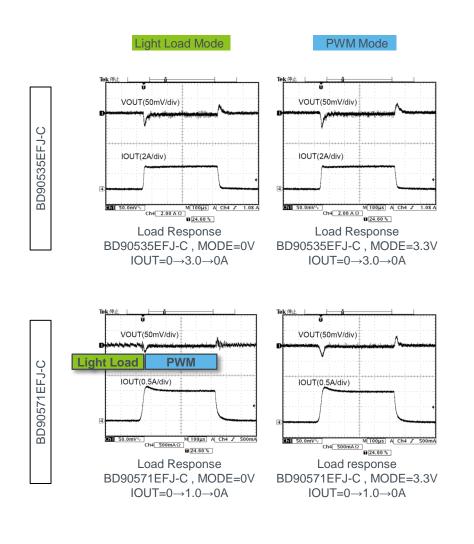


Reference Application Circuit



Switching Operation at Light Load Mode Switching Operation at PWM Mode

Load Response for Each Mode



SLLMTM control at light loads differs from regular PWM, resulting in higher output ripple voltage.

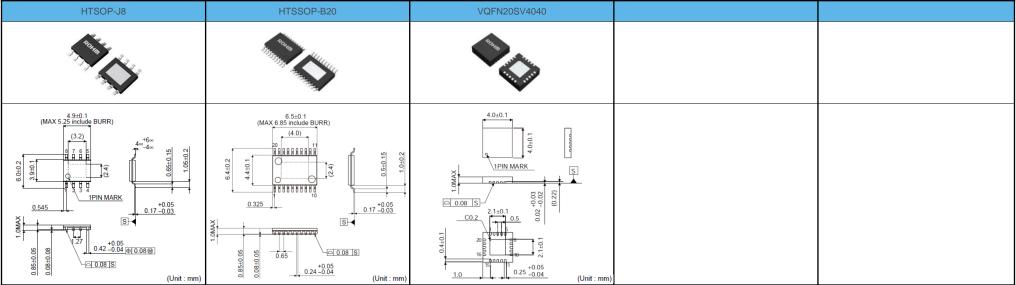
Also, during SLLM™ the transient response for heavy loads is slower.

Secondary Switching Regulator Selection Guide

| | | | | | | | | | | | | | | | | | | Func | tions | | | | | |
|------------|--------------|--------------------------|---------------------|--------------------|-------------------------|--------------------------|------|----------------------|-------------------------------|--|----------------|--------------|-----------------|--------------------------------|---------|------------------|----------------------|----------------------|--------------|---------------------|-------------------|---------------------|-----------------------------------|--------------|
| | Part No. | Number of Channels | Outpu | ut FET | Rated Voltage (V) | Output Current (A) | | /oltage nge /) | Output Voltage (V) Typ. | Reference (Output) Voltage Accuracy | Switc Frequ | 0 | Control Mode | Operating Circuit Current (mA) | Good | nization | , E | inous ation | Light | urrent on | l vn | tage on | Operating Temperature Range | Package |
| | | | High Side (Typ.) | Low Side (Typ.) | | Max. | Min. | Max. | , , , , , | (%) | Range (MHz) | Accuracy (%) | | Typ. | Power (| External Synchro | Variable Soft Sta | Synchro Rectifica | imple ode | Over Cu Protecti | Therma Shutdov | Overvol Protecti | (°C) | |
| \star | BD90540EFV-C | 1 | Pch (90mΩ) | Nch (60mΩ) | 7 | 4.0 | 2.6 | 5.5 | Variable (0.6-5.0) | ±1.5 | 0.3~2.4 | ±15 | Current | 0.065 | 1 | 1 | 1 | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSSOP-B20 |
| \bigstar | BD90540MUV-C | 1 | Pch (90mΩ) | Nch (60mΩ) | 7 | 4.0 | 2.6 | 5.5 | Variable (0.6-5.0) | ±1.5 | 0.3~2.4 | ±15 | Current | 0.065 | 1 | 1 | 1 | 1 | 1 | SR | SR | 1 | -40 ~ 125 | VQFN20SV4040 |
| \star | BD90530EFV-C | 1 | Pch (90mΩ) | Nch (60mΩ) | 7 | 3.0 | 2.6 | 5.5 | Variable (0.6-5.0) | ±1.5 | 0.3~2.4 | ±15 | Current | 0.065 | 1 | 1 | 1 | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSSOP-B20 |
| \star | BD90530MUV-C | 1 | Pch (90mΩ) | Nch (60mΩ) | 7 | 3.0 | 2.6 | 5.5 | Variable (0.6-5.0) | ±1.5 | 0.3~2.4 | ±15 | Current | 0.065 | 1 | 1 | 1 | 1 | 1 | SR | SR | 1 | -40 ~ 125 | VQFN20SV4040 |
| | BD90532EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 3.0 | 2.69 | 5.5 | 1.2 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSOP-J8 |
| | BD90535EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 3.0 | 2.69 | 5.5 | 1.5 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSOP-J8 |
| | BD90538EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 3.0 | 2.69 | 5.5 | 1.8 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSOP-J8 |
| \star | BD90520EFV-C | 1 | Pch (90mΩ) | Nch (60mΩ) | 7 | 2.0 | 2.6 | 5.5 | Variable (0.6-5.0) | ±1.5 | 0.3~2.4 | ±15 | Current | 0.065 | 1 | 1 | 1 | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSSOP-B20 |
| \star | BD90520MUV-C | 1 | Pch (90mΩ) | Nch (60mΩ) | 7 | 2.0 | 2.6 | 5.5 | Variable (0.6-5.0) | ±1.5 | 0.3~2.4 | ±15 | Current | 0.065 | 1 | 1 | / | 1 | 1 | SR | SR | 1 | -40 ~ 125 | VQFN20SV4040 |
| | BD90522EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 2.0 | 2.69 | 5.5 | 1.2 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSOP-J8 |
| | BD90525EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 2.0 | 2.69 | 5.5 | 1.5 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSOP-J8 |
| | BD90528EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 2.0 | 2.69 | 5.5 | 1.8 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | 1 | 1 | SR | SR | 1 | -40 ~ 125 | HTSOP-J8 |
| | BD90571EFJ-C | 1 | Pch (85mΩ) | Nch (70mΩ) | 7 | 1.0 | 2.69 | 5.5 | 1.2 | (±2.0) | 2.25 | ±20 | Current | 0.65 | - | - | - | / | / | SR | SR | / | -40 ~ 125 | HTSOP-J8 |

▼ Under development

※ SR:Self Recovery



Technology for the automotive future

Buck-Boost Switching Regulator Controller (BD9035AEFV-C)

Low Voltage Operation

Buck-Boost Automatic Switching Control Solution for Low Voltage Drive

The BD9035 buck-boost switching controller features high withstand voltage, a wide input range (VIN=3.8 to 30V), and is capable of generating buck-boost output using only one inductor. Boost-buck automatic switching control improves efficiency over conventional REGSPIC switching regulators. In addition, high switching frequency accuracy ($\pm 7\%$) is achieved throughout the entire operating temperature range (Ta=-40 $^{\circ}$ C to +125 $^{\circ}$ C).

Product Overview: BD9035AEFV-C

Buck-boost output possible with only one inductor
Automatic Boost/Boost-Buck/Buck switching improves efficiency

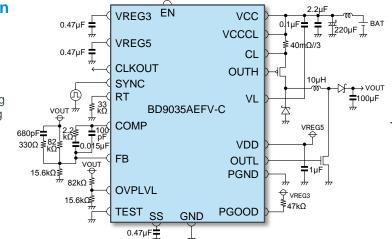
3-mode automatic switching control

High accuracy oscillation frequency and built-in PLL external synchronization function simplify noise countermeasures

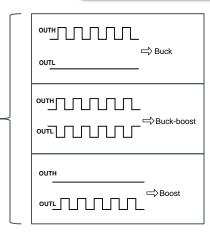
- High switching frequency accuracy: ±7% (-40°C to +125°C)
- PLL enables a wide external synchronous frequency range: 100k to 600kHz
- Input voltage range: 3.8V to 30V (40V rating)
- Oscillation frequency range: 100k to 600kHz
- Two-stage overcurrent protection through one external resistor
- Output undervoltage/overvoltage protection and Power Good



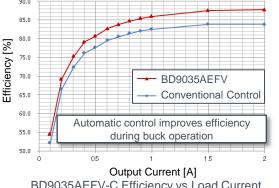
HTSSOP-B24



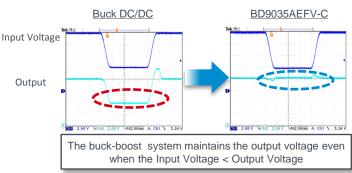
BD9035AEFV-C Application Circuit



External MOS Gate Waveforms for Each Mode



BD9035AEFV-C Efficiency vs Load Current VIN=12V, VOUT=6V, f=350kHz

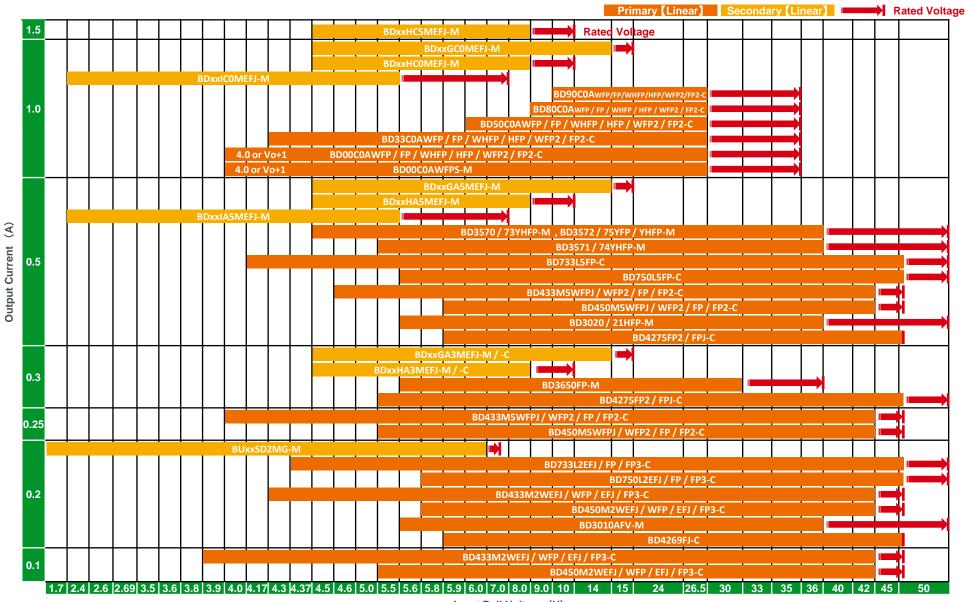


BD9035AEFV-C Input vs Output Voltage VIN=10V, VOUT=8V, f=350kHz

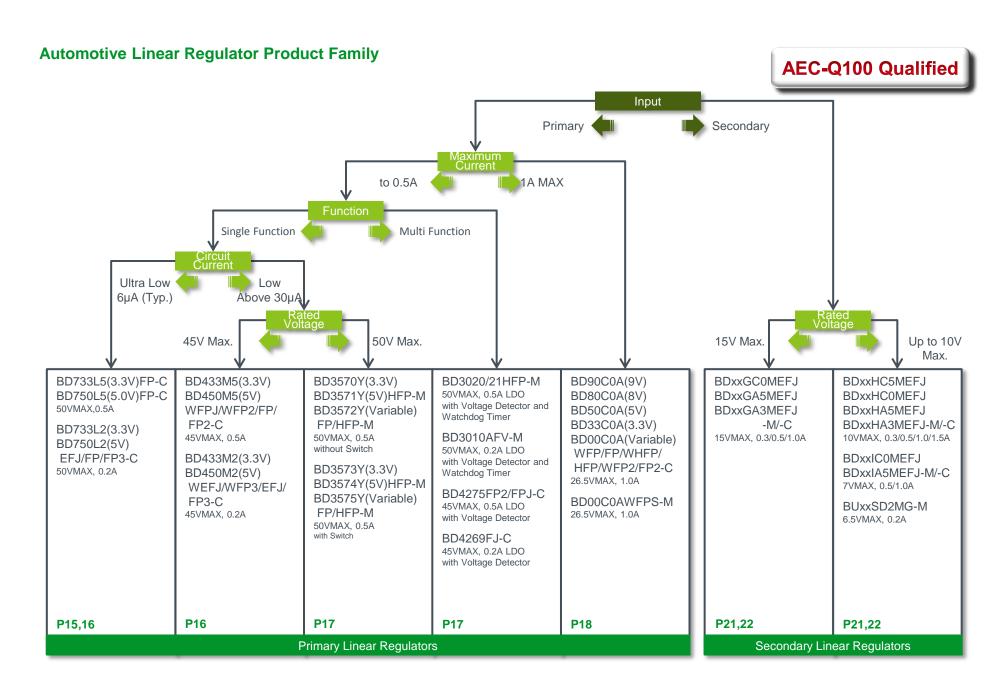
| | | | | | | | | | | | | | | | | | Func | tions | | | | | |
|-----------|-----------------------|-------------------|-------------------|------------------------------------|----------------------------------|------|----------------------|-------------------------------|--|----------------|--------------|-----------------|--------------------------------|-------|-----------------|--------------------|--------------------|------------------|-------------------|----------|-------------------|---|------------|
| Part No. | Numbe of Channe | | ontrol Method | Maximum Input Voltage (V) | Output Current (A) Max. | | /oltage nge /) | Output Voltage (V) Typ. | Reference (Output) Voltage Accuracy | Switc Frequ | | Control Mode | Operating Circuit Current (mA) | Bood | al onization | e art | onous ation | Light Iode | urrent ion | al wn | ltage ion | Operating Temperature Range (°C) | Package |
| | | Pch Controller | Nch Controller | (•) | Wax. | Min. | Max. | | (%) | Range (kHz) | Accuracy (%) | | Тур. | Power | Externa | Variabl Soft St | Synchr Rectific | Simple Load N | Over C Protect | Therm: | Overvo Protect | (0) | |
| BD9035AEF | /-C 1 | Push-Pull | Push-Pull | 40 | _ | 3.8 | 30 | Variable | ±1.5 | 100 ~ 600 | ±7 | Voltage | 7 | 1 | 1 | 1 | - | 1 | SR | SR | 1 | -40 ~ 125 | HTSSOP-B24 |

SR:Self Recovery

Automotive Step-Down Linear Regulator Lineup



Input Rail Voltage (V)



Primary Linear Regulators (BD7xxLxxxx-C Series)

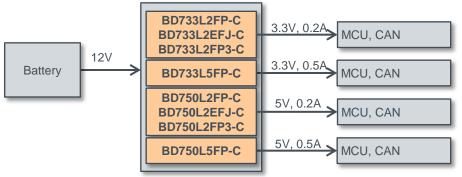
Low Quiescent Current

Low Quiescent Current Solutions

The BD7xxLxxxx-C series of low quiescent current regulators features a rated voltage of 50V, 200/500mA output current, an output voltage accuracy of $\pm 2\%$, and current consumption of only $6\mu A$ (Typ.).

There regulators are therefore ideal for applications requiring a direct connection to the battery and a low current consumption. Ceramic capacitors can be used for compensation of the output capacitor phase.

Furthermore, these ICs also feature overcurrent protection to protect the device from damage caused by short-circuiting and an integrated thermal shutdown to protect the device from Overheating at overload conditions.



Product Overview: BD7xxLxxxx-C

Ultra-low quiescent current: 6μA (Typ.)

• Output transistor : Low saturation Pch DMOS (3Ω Typ.)

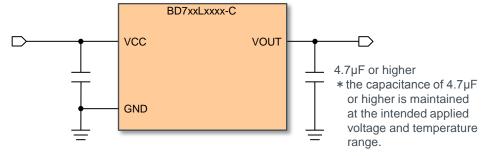
VCC maximum voltage : 50V

Output current : 200mA (Max.) / 500mA (Max.)

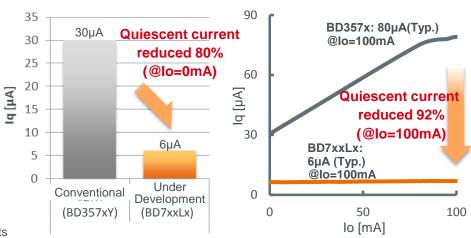
● Output voltage : 3.3V±2% / 5.0V±2%

- Enables low ESR ceramic capacitors to be used for output phase compensation
- Integrated output current control circuit protects the IC against damage due to short circuits
- Built-in thermal shutdown prevents IC overheating due to overload conditions.





BD7xxLxxxx-C Application Circuit



BD7xxLxxxx-C Quiescent Current Comparison

Primary Linear Regulator Selection Guide

| T | | | | | | | | | | | | | F | unction | S | | | |
|---------|---------------|-------------------------|---------------|----------------------|-------------------------------|----------------------------------|--------------------------------------|--------------------------------|------------------------------------|--------------------|--------|-----------------------|---------------|--------------------|-----|------------------|---|----------|
| | Part No. | Rated Voltage (V) | | /oltage nge V) | Output Voltage (V) Typ. | Output Current (A) Max. | Output Voltage Accuracy (%) | Dropout Voltage (V) Typ. | Circuit Current (µA) Typ. | Shutdown Switch | Others | able ction Voltage | ction Voltage | ction ıracy (%) | | WDT (Switchable) | Operating Temperature Range (°C) | Package |
| | | | Min. | Max. | | | | | | | | Variable Detection | 4.5V Dete | Deter Accu | WDT | WDT (Swir | | |
| | BD733L2FP-C | | | | | | | | | - | - | - | - | - | - | - | | TO252-3 |
| | BD733L2EFJ-C | | 4.37 | | 3.3 | 0.20 | | 0.60@0.20A | | - | - | - | - | - | - | - | | HTSOP-J8 |
| \star | BD733L2FP3-C | | | | 3.3 | | | | | - | - | - | - | - | - | - | | SOT223-4 |
| | BD733L5FP-C | 50 | 4.17 | 45 | | 0.50 | ±2 | 0.40@0.20A | 6 | - | - | - | - | - | - | - | -40∼+125@Ta | TO252-3 |
| | BD750L2FP-C | 30 | | 40 | | | | | Ü | - | - | - | - | - | - | - | 40 1120@1a | TO252-3 |
| | BD750L2EFJ-C | | 5.8 | | 5.0 | 0.20 | | 0.40@0.20A | | - | - | - | - | - | - | - | | HTSOP-J8 |
| \star | BD750L2FP3-C | | | | 0.0 | | | | | - | - | - | - | - | - | - | | SOT223-4 |
| Į | BD750L5FP-C | | 5.6 | | | 0.50 | | 0.25@0.20A | | - | - | - | - | - | - | - | | TO252-3 |
| | BD433M2EFJ-C | | 4.3 | | | | | | | - | - | - | - | - | - | - | | HTSOP-J8 |
| - | BD433M2FP3-C | | @0.20A | | | 0.20 | | 0.20@0.10A | 40 | - | - | - | - | - | - | - | | SOT223-4 |
| ı | BD433M2WEFJ-C | | 3.9 | | | 0.20 | | 0.20 @ 0.10/1 | 10 | Internal | - | - | - | - | - | - | | HTSOP-J8 |
| | BD433M2WFP3-C | | @0.10A | | 3.3 | | | | | Internal | - | - | - | - | - | - | | SOT223-4 |
| ı | BD433M5FP-C | | 4.6 | | 0.0 | | | | | - | - | - | - | - | - | - | | TO252-3 |
| 1 | BD433M5FP2-C | | @0.50A / | | | 0.50 | | 0.25@0.30A | 38 | - | - | - | - | - | - | - | | TO263-3 |
| ı | BD433M5WFP2-C | | 4.0 @0.25A | | | | | | | Internal | - | - | - | - | - | - | | TO263-5 |
| | BD433M5WFPJ-C | 45 | @0.25A | 42 | | | ±2 | | | Internal | - | - | - | - | - | - | -40~+150@Tj | TO252-J5 |
| ı | BD450M2EFJ-C | | 5.8 | | | | | | | - | - | - | - | - | - | - | | HTSOP-J8 |
| | BD450M2FP3-C | | @0.20A / | | | 0.20 | | 0.16@0.10A | 40 | - | - | - | - | - | - | - | | SOT223-4 |
| | BD450M2WEFJ-C | | 5.5 @0.10A | | | | | | | Internal | - | - | - | - | - | - | | HTSOP-J8 |
| | BD450M2WFP3-C | | © 0.10A | | 5.0 | | | | | Internal | - | - | - | - | - | - | | SOT223-4 |
| | BD450M5FP-C | | 5.9 | | | | | | | - | - | - | - | - | - | - | | TO252-3 |
| | BD450M5FP2-C | | @0.50A / | | | 0.50 | | 0.20@0.30A | 38 | - | - | - | - | - | - | - | | TO263-3 |
| | BD450M5WFP2-C | | 5.5 @0.25A | | | | | | | Internal | - | - | - | - | - | - | | TO263-5 |
| | BD450M5WFPJ-C | | 30.20/(| | | | | | | Internal | - | - | - | - | - | - | | TO252-J5 |

★Under development

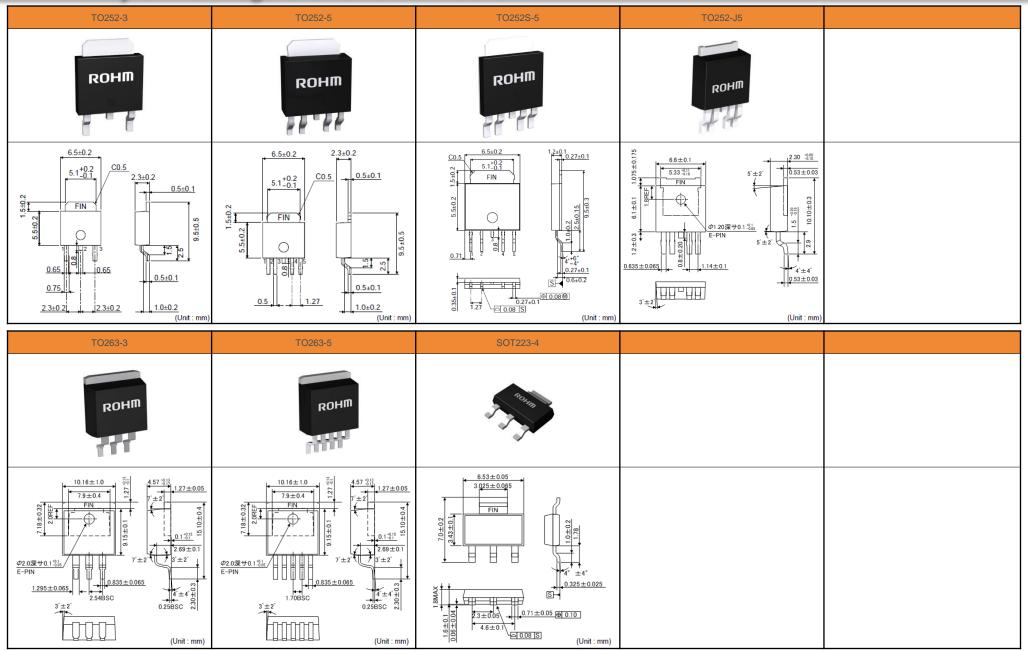
Primary Linear Regulator Selection Guide

| | | | | | | | | | | | | F | unction | S | | | |
|--------------|-------------------------|----------------------|------|-------------------------------|----------------------------------|--------------------------------------|--------------------------------|------------------------------------|--------------------|--------|-----------------------|-------------------|-----------------------|-----|---------------------|---|----------|
| Part No. | Rated Voltage (V) | Input V Rar (V | nge | Output Voltage (V) Typ. | Output Current (A) Max. | Output Voltage Accuracy (%) | Dropout Voltage (V) Typ. | Circuit Current (µA) Typ. | Shutdown Switch | Others | ole tion Voltage | tion Voltage | tion acy (%) | | WDT (Switchable) | Operating Temperature Range (°C) | Package |
| | | Min. | Max. | | | | | | | | Variable Detection | 4.5V Detection | Detection Accuracy | WDT | WDT (Switc | | |
| BD3572YFP-M | | 4.5 | | Variable (2.8-12.0) | | | 0.25@0.20A | | - | - | - | - | - | - | - | | TO252-5 |
| BD3575YFP-M | | 4.5 | | Variable (2.8-12.0) | | | 0.25@0.20A | | Internal | - | - | - | - | - | - | | 10252-5 |
| BD3570YHFP-M | | 4.5 | | 3.3 | | | - | | | - | - | - | - | - | - | | |
| BD3571YHFP-M | 50 | 5.5 | 36 | 5.0 | 0.50 | ±2 | 0.25@0.20A | 30 | - | - | - | - | - | - | - | -40∼+125@Ta | |
| BD3572YHFP-M | 50 | 4.5 | 30 | Variable (2.8-12.0) | 0.50 | ±2 | 0.25@0.20A | 30 | | - | - | - | - | - | - | -40~+125@1a | HRP5 |
| BD3573YHFP-M | | 4.5 | | 3.3 | | | - | | | - | - | - | - | - | - | | TIKES |
| BD3574YHFP-M | | 5.5 | | 5.0 | | | 0.25@0.20A | | Internal | - | - | - | - | - | - | | |
| BD3575YHFP-M | | 4.5 | | Variable (2.8-12.0) | | | 0.23@0.20A | | | - | - | - | - | - | - | | |
| BD3650FP-M | 36 | 5.6 | 30 | 5.0 | 0.30 | ±2 | 0.20@0.20A | 500 | - | - | - | - | - | - | - | -40∼+125@Ta | TO252-3 |
| BD3020HFP-M | F0 | 5.0 | 00 | 5.0 | 0.50 | | 0.00.004 | 00 | | | 1 | - | ±2 | 1 | - | 40 405@T | HRP7 |
| BD3021HFP-M | 50 | 5.6 | 36 | 5.0 | 0.50 | ±2 | 0.30@0.20A | 80 | - | - | - | 1 | ±2 | - | 1 | -40 ~ +125@Ta | HRP7 |
| BD3010AFV-M | 50 | 6.0 | 36 | 5.0 | 0.20 | ±2 | 0.25@0.15A | 80 | - | - | 1 | | ±3 | | 1 | -40~+125@Ta | SSOP-B20 |
| BD4275FP2-C | 45 | 5.5@0.3A | 45 | 5.0 | 0.50 | | 0.05@0.00; | 0.5 | | | | / | -2.6 | | | -40∼+125@Ta | TO263-5 |
| BD4275FPJ-C | 45 | 5.9@0.5A | 45 | 5.0 | 0.50 | ±2 | 0.25@0.30A | 65 | • | - | - | 4.62 V | +2.8 | - | - | -40~+150@Tj | TO252-J5 |
| BD4269FJ-C | 45 | 5.5 | 45 | 5.0 | 0.20 | ±2 | 0.25@0.10A | 70 | 1 | - | 1 | - | ±2.6 | - | - | -40~+125@Ta -40~+150@Tj | SOP-J8 |

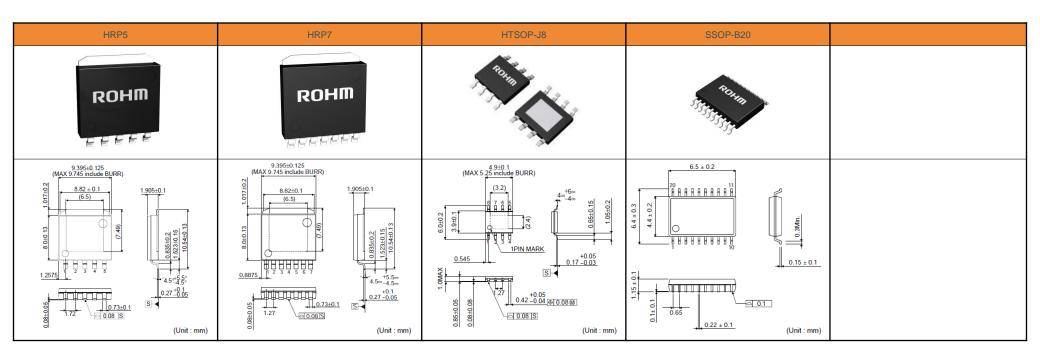
| | | | | | | | | | | | | F | unction | S | | | |
|---------------------------|-------------------------|----------------------|------|-------------------------------|----------------------------------|--------------------------------------|--------------------------------|------------------------------------|--------------------|--------|-----------------------|---------------|-----------------------|-----|---------------------|---|-----------------|
| Part No. | Rated Voltage (V) | Input \ Rar (\ | | Output Voltage (V) Typ. | Output Current (A) Max. | Output Voltage Accuracy (%) | Dropout Voltage (V) Typ. | Circuit Current (µA) Typ. | Shutdown Switch | Others | ole tion Voltage | ction Voltage | tion acy (%) | | WDT (Switchable) | Operating Temperature Range (°C) | Package |
| | | Min. | Max. | | | | | | | | Variable Detection | 4.5V Detec | Detection Accuracy | WDT | WDT (Switc | | |
| BD00C0AWFPS-M | 35 | 4.0 or Vo+1.0 | 26.5 | Variable (3.0-15.0) | 1.00 | ±3 | 0.30 @0.50A Vo≥5.0 | 500 | Internal | - | - | - | - | - | - | -40∼+105@Ta | TO252S-5 |
| BD33C0AFP-C | | | | | | | | | - | - | - | - | - | - | - | | TO252-3 |
| BD33C0AFP2-C | | 4.3 | | 3.3 | | | - | | - | - | - | - | - | - | - | | TO263-3 |
| BD33C0AHFP-C | | | | | | | | | - | - | - | - | - | - | - | | HRP5 |
| BD50C0AFP-C | | | | | | | | | - | - | - | - | - | - | - | | TO252-3 |
| BD50C0AFP2-C | | 6.0 | | 5.0 | | | | | - | - | - | - | - | - | - | | TO263-3 |
| BD50C0AHFP-C | | | | | | | | | - | - | - | - | - | - | - | | HRP5 |
| BD80C0AFP-C | | | | | | | 0.30 | | - | - | - | - | - | - | - | | TO252-3 |
| BD80C0AFP2-C | | 9.0 | | 8.0 | | | @0.50A | | - | - | - | - | - | - | - | - | TO263-3 |
| BD80C0AHFP-C | | | | | | | | | - | - | - | - | - | - | - | | HRP5 |
| BD90C0AFP-C | | 40.0 | | | | | | | - | - | - | - | - | - | - | | TO252-3 |
| BD90C0ALIER C | | 10.0 | | 9.0 | | | | | - | - | - | - | - | - | - | | TO263-3 |
| BD90C0AHFP-C BD00C0AWFP-C | | | | | | | | | - | - | - | - | - | - | - | | HRP5 TO252-5 |
| BD00C0AWFP-C | 35 | 4.0 | 26.5 | \/ariable (4.0.45.0) | 1.00 | ±3 | 0.30@0.50A | 500 | | - | - | - | - | - | - | -40 ∼ +125@Ta | TO252-5 |
| BD00C0AWFP2-C | 35 | or Vo+1.0 | 20.5 | Variable (1.0-15.0) | 1.00 | ±3 | Vo ≥ 5.0 | 500 | | - | - | - | - | - | - | -40~+125@1a | HRP5 |
| BD33C0AWFP-C | | | | | | | | | | - | - | _ | | | _ | | TO252-5 |
| BD33C0AWFP2-C | | 4.3 | | 3.3 | | | _ | | | | _ | _ | | _ | _ | | TO252-5 |
| BD33C0AWHFP-C | | 4.5 | | 3.3 | | | | | | | _ | _ | | _ | _ | | HRP5 |
| BD50C0AWFP-C | | | | | | | | | | - | _ | _ | _ | _ | _ | | TO252-5 |
| BD50C0AWFP2-C | | 6.0 | | 5.0 | | | | | Internal | - | _ | _ | _ | _ | _ | | TO263-5 |
| BD50C0AWHFP-C | | 2.0 | | | | | | | | - | _ | _ | _ | _ | _ | | HRP5 |
| BD80C0AWFP-C | | | | | | | | | | - | - | - | _ | - | _ | | TO252-5 |
| BD80C0AWFP2-C | | 9.0 | | 8.0 | | | 0.30@0.50A | | | - | - | - | - | - | - | | TO263-5 |
| BD80C0AWHFP-C | | | | | | | | | | - | - | - | - | - | - | | HRP5 |
| BD90C0AWFP-C | | | | | | | | | | - | - | _ | - | - | _ | | TO252-5 |
| BD90C0AWFP2-C | | 10.0 | | 9.0 | | | | | | - | - | - | - | - | - | | TO263-5 |
| BD90C0AWHFP-C | | | | | | | | | | - | - | - | - | - | - | | HRP5 |

Technology for the automotive future

Primary Linear Regulator Selection Guide

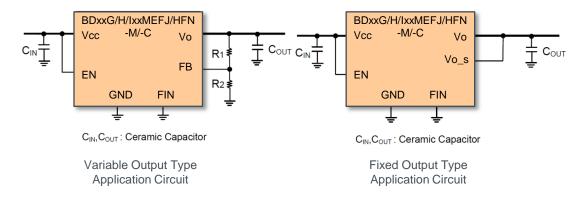


Technology for the automotive future



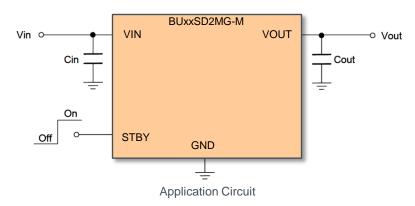
[BDxxG/H/IxxMEFJ/HFN-M/-C]

→ Absolute Maximum Rating G:15V H:10V I:7V

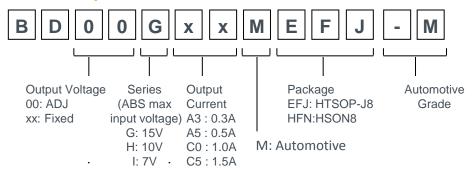


[BUxxSD2MG-M]

→ Absolute Maximum Rating 6.5V



Part No. Explanation/Overview: BDxxG/H/IxxEEFJ/HFN-M/-C



- Shutdown circuit current: 0µA (Typ.)
- Output voltage accuracy: -M=±3% (Ta: -40°C ~ +105°C) -C=±2% (Ta: -40°C ~ +125°C)
- Internal standby function
- Enables the use of low ESR ceramic capacitors for output phase compensation (1.0µF Min.)
- Integrated output current control circuit protects the IC from damage due to output short circuits
- Built-in thermal shutdown to prevents IC overheating during overload

Product Overview: BUxxSD2MG-M

- Input Voltage Range: 1.7V to 6.0V (6.5V Rating)
- Low Quiescent Current: 35µA (Typ.)
- Output Current: 200mA (Max.)
- Output Voltage Accuracy: ±2% (Ta: -40°C to +105°C)
- Low Output Noise: 30µVrms (10-100kHz)
- High PSRR (Ripple Rejection): 70dB Typ.@1kHz
- Integrated Standby function
- Enables the use of low ESR ceramic capacitors for output phase compensation (0.47µF Min.)
- Integrated overcurrent protection protects the IC from damage due to output short circuits
- Built-in thermal shutdown function prevents IC overheating during overload

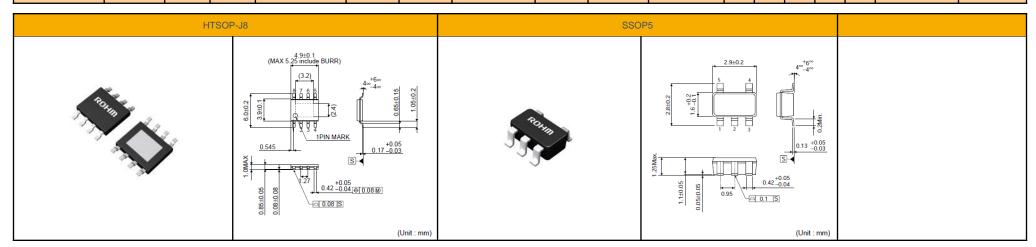






Secondary Linear Regulator Selection Guide

| | | | | | | | | | | | | F | unction | S | | | |
|---------------|---------------------------|------|----------------------|---|----------------------------------|--------------------------------------|--|------------------------------------|--------------------|--------|-----------------------|---------------|----------------|-----|---------------------|---|----------|
| Part No. | Maximum Voltage (V) | Ra | Voltage nge V) | Output Voltage (V) Typ. | Output Current (A) Max. | Output Voltage Accuracy (%) | Dropout Voltage (V) Typ. | Circuit Current (µA) Typ. | Shutdown Switch | Others | ole tion Voltage | ction Voltage | tion acy(%) | | WDT (Switchable) | Operating Temperature Range (°C) | Package |
| | | Min. | Max. | | | | | | | | Variable Detection | 4.5V Detec | Detec Accur | WDT | WDT (Switc | | |
| BDxxGC0MEFJ-M | 15 | 4.5 | 14 | Variable (1.5-13.0) | 1.00 | ±3 | 0.60@1.00A | 600 | Internal | - | - | - | - | - | - | | HTSOP-J8 |
| BDxxGA5MEFJ-M | 15 | 4.5 | 14 | / 1.5 / 1.8 / 2.5 / 3.0 / 3.3 / 5.0 / 6.0 / 7.0 / 8.0 | 0.50 | ±3 | 0.60@0.50A | 600 | Internal | - | - | - | - | - | - | -40∼+105@Ta | HTSOP-J8 |
| BDxxGA3MEFJ-M | 15 | 4.5 | 14 | / 9.0/10.0/12.0 | 0.30 | ±3 | 0.60@0.30A | 600 | Internal | - | - | - | - | - | - | | HTSOP-J8 |
| BDxxGA3MEFJ-C | 15 | 4.5 | 14 | Variable / 3.3 / 5.0 | 0.30 | ±2 | 0.60@0.30A | 600 | Internal | - | - | - | - | 1 | 1 | -40∼+125@Ta | HTSOP-J8 |
| BDxxHC5MEFJ-M | 10 | 4.5 | 8.0 | | 1.50 | ±3 | 0.60@1.50A | 600 | Internal | - | - | - | - | - | - | | HTSOP-J8 |
| BDxxHC0MEFJ-M | 10 | 4.5 | 8.0 | Variable (1.5-7.0) | 1.00 | ±3 | 0.60@1.00A | 600 | Internal | - | - | - | - | - | - | -40∼+105@Ta | HTSOP-J8 |
| BDxxHA5MEFJ-M | 10 | 4.5 | 8.0 | / 1.5 / 1.8/ 2.5 / 3.0 / 3.3/ 5.0 | 0.50 | ±3 | 0.60@0.50A | 600 | Internal | - | - | - | - | - | - | -40~+105@1a | HTSOP-J8 |
| BDxxHA3MEFJ-M | 10 | 4.5 | 8.0 | / 6.0 / 7.0 | 0.30 | ±3 | 0.60@0.30A | 600 | Internal | - | - | | - | - | - | | HTSOP-J8 |
| BDxxHA3MEFJ-C | 10 | 4.5 | 8.0 | | 0.30 | ±2 | 0.60@0.30A | 600 | Internal | - | - | - | - | - | - | -40∼+125@Ta | HTSOP-J8 |
| BDxxIC0MEFJ-M | 7 | 2.4 | 5.5 | Variable (0.8-4.5) / 1.0 / 1.2 / 1.5 | 1.00 | ±3 | 0.40@1.00A | 250 | Internal | - | - | - | - | 1 | 1 | -40∼+105@Ta | HTSOP-J8 |
| BDxxIA5MEFJ-M | 7 | 2.4 | 5.5 | / 1.8 / 2.5 / 3.0 / 3.3 | 0.50 | ±3 | 0.40@0.50A | 250 | Internal | - | - | - | - | 1 | - | -40~+105@1a | HTSOP-J8 |
| BUxxSD2MG-M | 6.5 | 1.7 | 6.0 | 1.2 / 1.5 / 1.8 / 2.5 / 2.8 / 3.0 / 3.3 | 0.20 | ±2 | 0.28 / 0.18 / 0.15 / 0.10 / 0.085 @0.10A | 33 | Internal | - | - | - | - | - | - | -40~+105@Ta | SSOP5 |



Leveraging Power Supply IC Characteristics



Thermal Resistance - Characteristics

The following definitions comply with JEDEC Standard JESD51

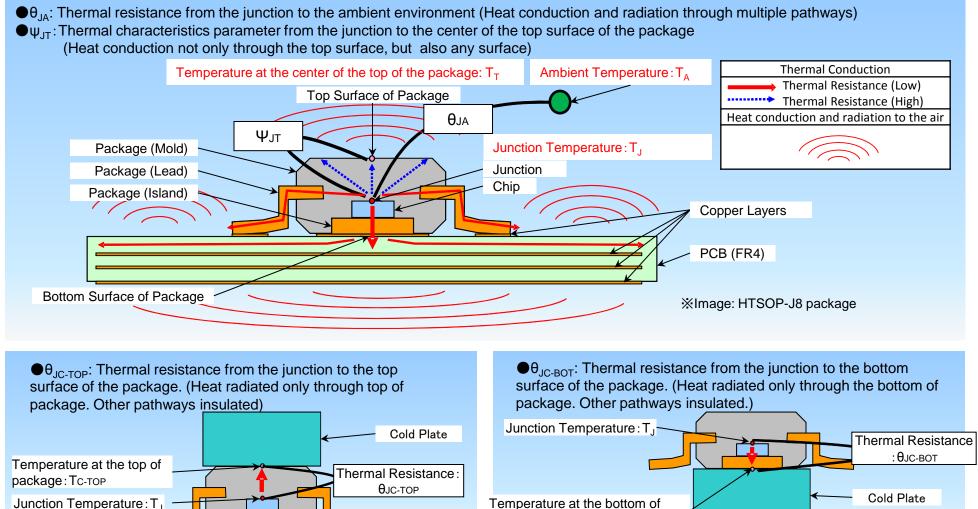
| Symbol | Definition | Applications | Formula |
|---------------------|--|--|--|
| θ_{JA} | Thermal resistance between junction temperature (T_J) and ambient temperature (T_A) when the package is mounted on a PCB. | Comparison of heat dissipation characteristics between different packages. | $\theta_{JA} = (T_J - T_A) / P$ |
| $\Psi_{ m JT}$ | Thermal characteristics parameter representing the temperature difference between the junction temperature (T_J) and the temperature of the center of the top surface of the package (T_T), caused by the power consumption (P) of the device. | Estimation of the junction temperature. | $\Psi_{JT} = (T_J - T_T) / P$ |
| θ _{JC-TOP} | The thermal resistance between the junction temperature (T_J) and the top surface of the package (T_{C-TOP}) . Heat is dissipated only through the top surface of the package – all other pathways are insulated. | Can be used in simulations using the 2-resistance model. | $\theta_{\text{JC-TOP}} = (T_{\text{J}} - T_{\text{C-TOP}}) / P$ |
| θ _{ЈС-ВОТ} | The thermal resistance between the junction temperature (T_J) and the bottom surface of the package $(T_{C\text{-BOT}})$. Heat is dissipated only through the bottom surface of the package – all other pathways are insulated. | Used to estimate the junction temperature for packages where metal is exposed at the bottom for heat dissipation, since the majority of heat is dissipated through the bottom. | $\theta_{\text{JC-BOT}} = (T_{\text{J}} - T_{\text{C-BOT}}) / P$ |

Note 1: θ_{JA} and Ψ_{JT} are obtained when mounted on a JEDEC board.

Note 2: Data provided as θ_{JC} conventionally is Ψ_{JT} in this definition.

Thermal Resistance - Characteristics

Cf. JEDEC (JESD51)



package: Тс-вот

Leveraging Power Supply IC Characteristics



Noise Characteristics - Tolerance

EMC (Electromagnetic Compatibility)

It is important that electronic devices: 1) Do not interfere with other devices, and 2) Are able to maintain normal performance even when receiving interference.

The need to balance both of these requirements gives rise to the term Electromagnetic Compatibility, which can be broken down into 2 components – EMI and EMS.

EMI (<u>E</u>lectro<u>m</u>agnetic <u>I</u>nterference – Emission)

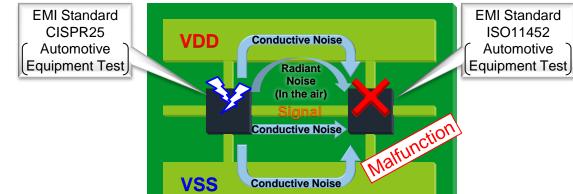
Operating the target IC may cause noise to be generated, which can lead to operation stoppage due to system and/or peripheral circuit malfunction.

To prevent this, delicate, complex circuit design is necessary.

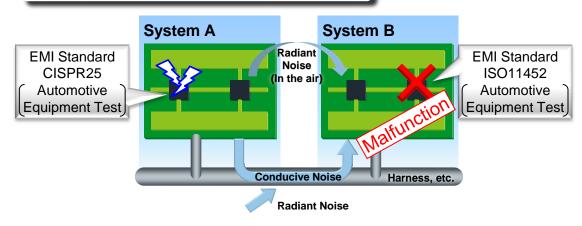
EMS (Electromagnetic Susceptibility – Immunity)

Conversely, peripheral IC and/or system operation may generate noise which can interfere with the target IC and lead to malfunction and cause operation to fail. In this case robust circuit design is required.

EMC problems on the same board



EMC malfunction from external interference



PCB Design Checkpoints

Possible Concerns Regarding Improper DC/DC PCB Layout

EMC performance, PI (Power Integrity) decrease

Deterioration in basic performance (i.e. output voltage accuracy)

Unstable operation (e.g. oscillation, SW waveform splitting)

Recommendations on PCB Design

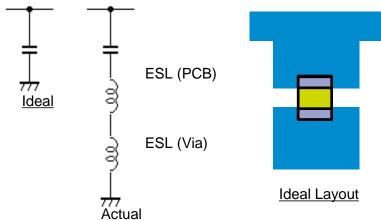
- 1. Power lines should be as short and wide as possible.
- 2. Please place the input decoupling (ceramic) capacitor as close as possible to the IC power supply-GND pin. (GND side of the IC power supply-SBD for chopper types) ⇒ Shortest AC current path
- 3. The resistor RT for determining the oscillating frequency should be located near to the GND pin (reference GND).
- 4. Position the feedback resistor for variable output voltage types as close to the feedback pin, shortening the wiring from the feedback resistor to the feedback terminal.
- 5. The feedback resistor should be located far from noise sources such as inductors and switching lines. It is good practice for dual-sided boards to place power components on the same side as the IC and the rest of the components on the other side. (When doing so, please do not pass the feedback line under the inductor.)
- 6. Separating power GND (SBD, input/output capacitor GND) and reference GND (RT, GND) will minimize the effects of switching noise. However, please make them common through a GND plane.
- 7. Do not use thermal relief whenever possible.
 - ⇒ Deteriorating high frequency characteristics

Notes on Thermal Relief

Please consider capacitor layout to minimize noise.



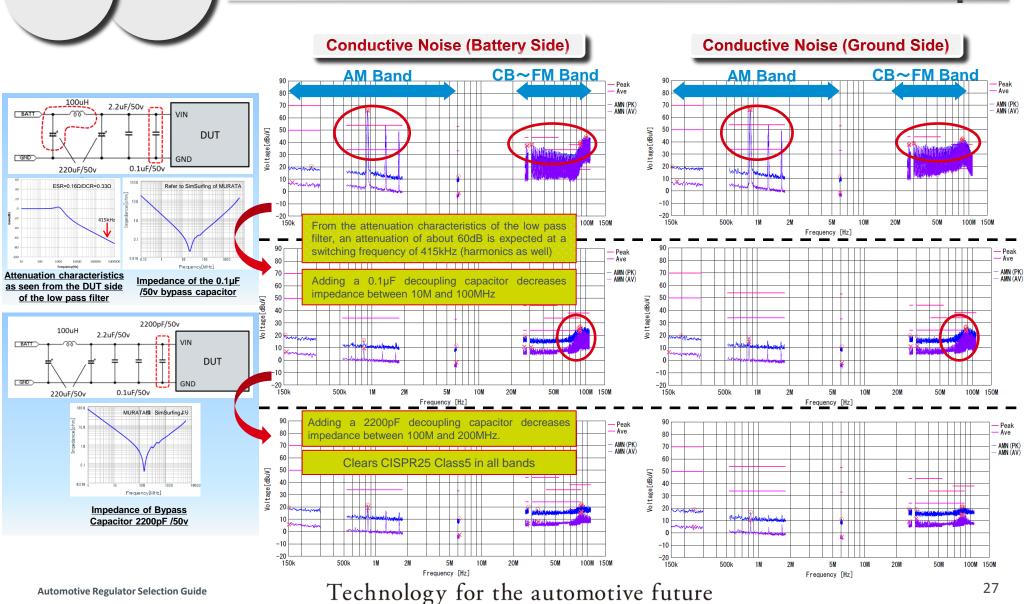
In the above layout since the ESL component of the PCB is added, the resonant frequency from the formula on Page 28 moves to the low frequency side. As a result, it may not be possible to achieve the desired noise removal effects.



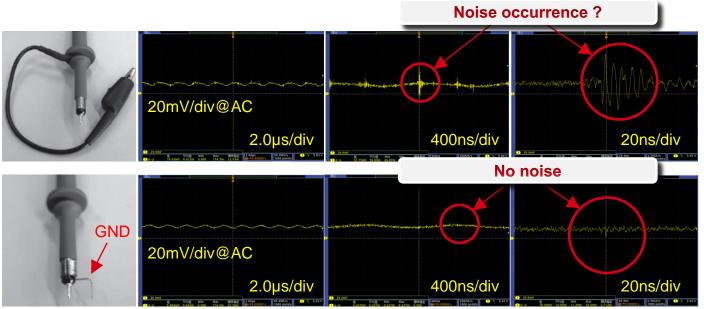


Automotive Regulator Selection Guide

Thermal Noise Conductive Noise Countermeasure Example



Appendix: Waveform Data Acquisition Techniques



The waveforms at the top were obtained using a GND lead attached to a probe.

The waveforms below were taken by measuring the output ripple voltage using a tin plated wire wound to GND in a short distance.

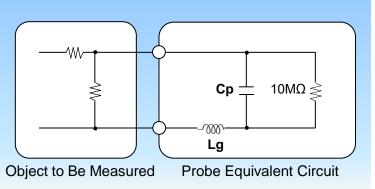
Although at 2.0µs/div there does not appear to be a large difference, upon magnification spikes can be seen.

These spikes are largely due to the resonance of the inductance of the GND lead and capacitance of the probe input.

Resonant Frequency =
$$\frac{1}{2^*\pi\sqrt{(Lg * Cp)}}$$

Lg: Probe Ground Lead Inductance

Cp: Probe Input Capacitance



In order to prevent such resonance, optimized probing must be conducted where Cp and Lg in the equivalent circuit is as small as possible.

Since the input capacitance of a standard passive probe is limited to around 10pF, it is clear that reducing the GND lead inductance Lg (nH/mm) will lead to improved measurement accuracy. In order to further improve accuracy use of an active probe (FET probe) is recommended.

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